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				Application Number	10/589,399-Conf. #7126
				Filing Date	August 14, 2006
				First Named Inventor	Guo-Quan LU
				Art Unit	1793
				Examiner Name	Y. Takeuchi
				Attorney Docket Number	124617.0118
Sheet	1	of	2		

[illegible]

FOREIGN PATENT DOCUMENTS					
Examiner Initials*	Cite No. ¹	Foreign Patent Document	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages Or Relevant Figures Appear
		Country Code ³ -Number ⁴ -Kind Code ⁵ (if known)			
	E	JP-07126556-A	05-16-1995	Tatsuta Densen Kk	

Examiner Signature	/Yoshitoshi Takeuchi/ (01/08/2010)	Date Considered	01/08/2010
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Dated: November 22, 2009

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PTO/SB/08b (07-09)

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Sheet	2	of	2	Attorney Docket Number	124617.0118

NON PATENT LITERATURE DOCUMENTS			
Examiner Initials	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
	J	Wong Wai Kwan et al. "Low temperature sintering process for deposition of nano-structured metal for nano IC packaging". ELECTRONICS PACKAGING TECHNOLOGY, 2003 pp. 551-556.	
	K	Zhang Zhiye et al. "Nanoscale silver sintering for high-temperature packaging of semiconductor devices". MATERIALS PROCESSING AND MANUFACTURING DIVISION. 2004, pp. 129-135.	
	L	Zhiye Zhang et al. "Pressure-Assisted Low-Temperature Sintering of Silver Paste as an Alternative Die-Attach Solution to Solder Reflow". IEEE TRANSACTIONS ON ELECTRONICS PACKAGING MANUFACTURING. VOL. 25, NO. 4, OCTOBER 2002 pp. 279-283.	

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